

More than a Viewer

Laker Blitz is built on the production-proven technology platform used by the Laker Custom Layout System, upon which hundreds of companies have deployed design flows to produce high-quality chip layouts. It leverages the core suite of Laker layout editing commands and its unmatched tool interoperability to help designers reduce the time and effort required to achieve final tapeout.

Core Features

- Built-in net highlighting capabilities to rapidly trace critical nets for full-chip debug
- Automated data manipulation with extensive library of Laker Tcl extensions
- Integrated DRC with all top tier sign-off tools, including Calibre and IC Validator
- Laker APIs for internal chip finishing and third-party tool integration

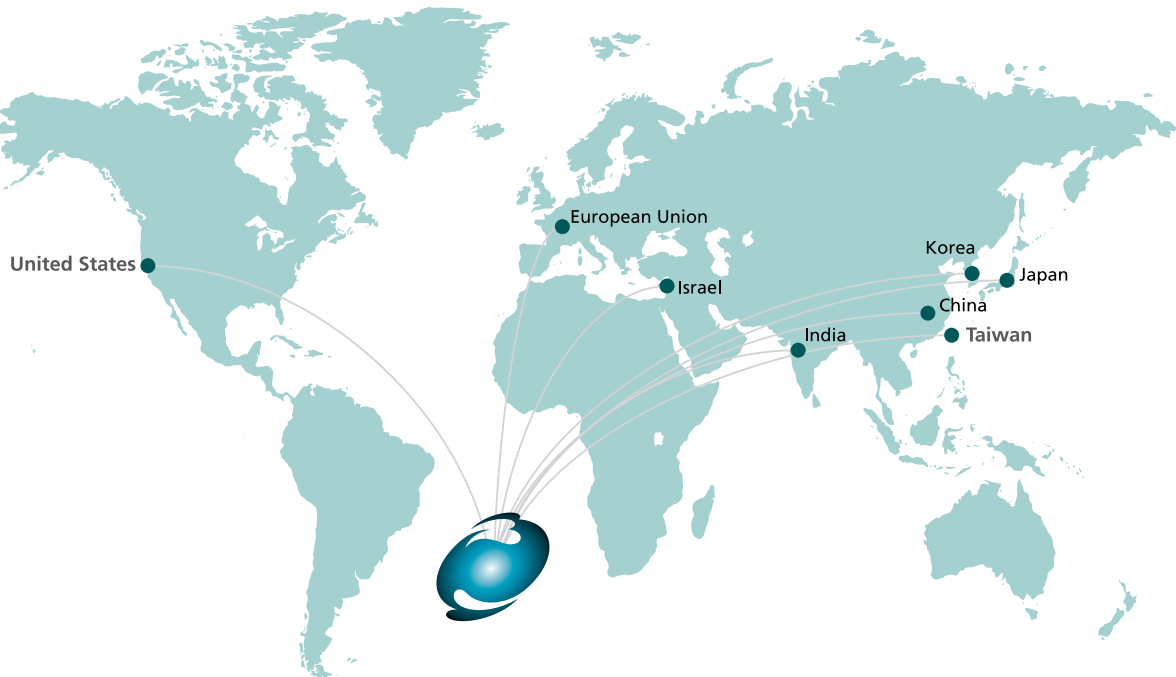
Superior Results with Less Effort

As with all software offerings in SpringSoft's popular family of Laker custom IC design and layout automation solutions, Laker Blitz enables users to achieve superior results with less effort. It brings together high-speed, high capacity database and proven layout technologies that are ideally suited to the chip finishing requirements of semiconductor manufacturers, foundries and fabless design companies.

Laker layout users appreciate the familiar, easy-to-use graphical interface of the Laker Blitz environment and its compatibility with existing Laker data manipulation and DRC capabilities. New Laker users realize measurable improvements in productivity with more robust chip-level editing features compared to the limited scope of current generation 'flat' layout viewers. At the same time, all Laker Blitz users benefit from the dramatic performance boost optimized for critical chip finishing tasks that are otherwise painfully slow with conventional layout editors or an impractical use of expensive digital place-and-route tools.

For information about the complete line of Laker products, visit: [Laker Custom IC design solutions](#).

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