



News Bulletin

Date: December 7, 2008

Subject: Download New Custom IC Design White Paper

SAN JOSE, Calif. — [SpringSoft, Inc.](#), a global supplier of specialized IC design software, has published a new white paper on schematic-driven layout, an innovative design methodology that assists designers with the physical implementation of complex custom circuits by providing automation and continuity between logic and layout domains.

- [Schematic-driven Layout Automation](#): Speeds the creation of custom layout by automating time-consuming portions of the flow while maintaining user control over the results. An integrated set of graphical windows enable the display and manipulation of the schematics, design hierarchy, and layout. Advanced SDL relies on device generation technology to automatically create the physical layout from schematic elements, providing the next big leap in productivity.

The SDL white paper is complemented by previously published papers on the use of parameterized devices and process design kits:

- [Laker Custom Layout Parameterized Devices](#): A parameterized device is used in the design of analog and custom digital ICs to take the place of many fixed cells by allowing the substitution of different values for specified dimensional variables (parameters). This saves an enormous amount of manual layout time.
- [Process Design Kits for Custom IC Design](#): Process Design Kits (PDKs) enable IC designers to achieve faster time to market and first-pass silicon success for designs that function as intended, offer less risk, and yield more predictable results. PDKs are developed specifically for each manufacturer (pure-play foundry), wafer process flow, and technology node to convey manufacturing requirements (physical and electrical design rules) and device performance.

The unique automation technologies described in these white papers are delivered with the Springsoft [Laker™ Custom Layout Automation System](#). More than 300 companies worldwide, including 5 of the top 10 semiconductor companies, have adopted the Laker system for designs down to 45 nanometers.

All white papers are immediately available for download from the SpringSoft web site at: <http://www.springsoft.com/technology/technical-papers/ic-design-technical-papers>

About SpringSoft

SpringSoft, Inc. (TAIEX: 2473) is a global supplier of specialized automation technologies that accelerate engineers during the design, verification and debug of complex digital, analog and mixed-signal ICs, ASICs, microprocessors, and SoCs. Its award-winning product portfolio features the Novas™ Verification Enhancement and Laker Custom IC Design solutions used by more than



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400 of today's leading IDM and fabless semiconductor companies, foundries, and electronic systems OEMs. Headquartered in Hsinchu, Taiwan, and San Jose, California, SpringSoft is the largest company in Asia specializing in IC design software and a recognized industry leader in customer service with more than 400 employees located in multiple R&D sites and local support offices around the world. For more information, visit www.springsoft.com.

Related Links:

- Learn more about SpringSoft, Inc. <http://www.springsoft.com/>
- Check out the Laker Layout Automation System
<http://www.springsoft.com/products/custom-design-and-layout/laker-custom-layout-system>
- Read the latest news and articles about custom IC design
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